Notice of Allowability	Application No.	Applicant(s)	
	10/687,591	TAKEMAE, YOSHIHIRO	
	Examiner	Art Unit	
	Daniel Kim	2185	
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	olication. If not include will be mailed in due	ed course. THIS
1. \boxtimes This communication is responsive to <u>8/7/06</u> .	<i>,</i>	•	
2. X The allowed claim(s) is/are 16-27 (renumbered as 1-12).			
3. Acknowledgment is made of a claim for foreign priority una a)	been received. been received in Application No cuments have been received in this rec	national stage applicational stage applicational stage application of the front (not the di).	quirements IOTICE OF
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 10/20/03 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. ☐ Notice of Informal P 6. ☐ Interview Summary Paper No./Mail Dat 7. ☑ Examiner's Amendr 8. ☑ Examiner's Stateme 9. ☐ Other	atent Application (PTO-413), te nent/Comment ent of Reasons for Allo	10/24/06
		PRIMARY EXAM	MINER

Examiner's Comment

Correction to Previous Office Action

1. In the Office Action mailed February 7, 2006 the examiner noted that the claim for priority under 35 U.S.C. 119(a)-(d) or (f) was approved because the priority document had been received.

However, upon further consideration it is apparent that the priority document was not received in the office. Therefore, this status has been corrected in the present Office
Action to show that the certified document has not yet been received in the office.

Information Disclosure Statement

2. The information disclosure statement (IDS) submitted on October 20, 2003, which the examiner has already considered, failed to positively indicate the status of the "Semiconductor Memories" non-patent literature. Therefore, an updated copy of this IDS is being provided to reflect its correct status. The updated IDS shows that this reference has <u>not</u> been considered by the examiner because no copy of the references were found to have been submitted, either filed in this patent application or in any related application.

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Examiner's Amendment

3. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Charles Marmelstein, registration number 25,895 on October 24, 2006.

4. In the claims:

Claim 1, line 1,

replace "capable of" with --for--;

Claim 26, line 2,

replace "capable of" with --for--.

Reasons for Allowance

5. The following is an examiner's statement of reasons for allowance:

In independent claims 16 and 26 directed towards a common memory controller for controlling different types of memory chips which are connected to a common bus, the following features taken in combination with the remaining limitations in the independent claims are not found in and/or are not obvious in view of the prior art of record, giving the language "in response to a specification of each of said memory

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chips" the scope of meaning as disclosed in the specification and drawings at paragraphs [0067] and [0084-0085] and in Fig. 5.

- 6. In claim 16, "a conversion control portion for converting said control output signal into a memory input signal in response to a specification of each of said memory chips to which the memory controller is connected, and converting a memory output signal output from the memory chips into the control input signal receivable to said controller".
- 7. In claim 26, "a conversion control portion for converting said control output signal into a memory input signal in response to a specification of each of the memory chips, and converting a memory output signal output from the memory chips into the control input signal receivable to said controller".
- 8. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance".

Contact Information

9. Any inquiries concerning this action or earlier actions from the examiner should be directed to Daniel Kim, reachable at 571-272-2742, on Mon-Fri from 10:00am-

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6:30pm. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sanjiv Shah, is also reachable at 571-272-4098.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information from published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. All questions regarding access to the Private PAIR system should be directed to the Electronic Business Center (EBC), reachable at 866-217-9197.

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STEPHEN C. ELMORE DOMARY EXAMINER